

UNISONIC TECHNOLOGIES CO., LTD

TGBR10S150C

Preliminary

DIODE

DUAL TRENCH MOS SCHOTTKY BARRIER RECTIFIER

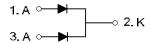
■ DESCRIPTION

The UTC **TGBR10S150C** is a dual trench mos schottky barrier rectifier, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

■ FEATURES

- *Super low forward voltage drop
- * High switching speed

■ SYMBOL

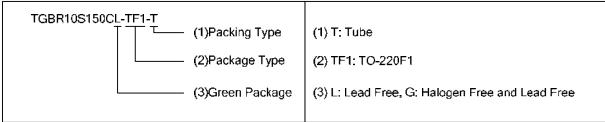




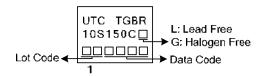
ORDERING INFORMATION

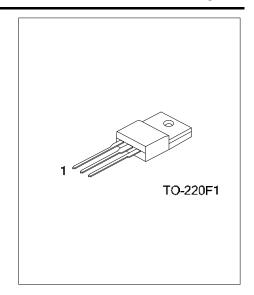
Ordering Number		Dookogo	Pin Assignment			Packing	
Lead Free	Halogen Free	Package	1	2	3	Facking	
TGBR10S150CL-TF1-T	TGBR10S150CG-TF1-T	TO-220F1	Α	K	Α	Tube	

Note: Pin Assignment: A: Anode K: Cathode



MARKING





www.unisonic.com.tw 1 of 3

■ **ABSOLUTE MAXIMUM RATINGS** (T_A=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER		SYMBOL	RATINGS	UNIT
DC Blocking Voltage		V_{RM}	150	V
Working Peak Reverse Voltage		V_{RWM}	150	V
Peak Repetitive Reverse Voltage		V_{RRM}	150	V
Average Restified Output Current Per Device	er Leg		5	Α
Average Rectified Output Current Per Device	otal	Io	10	Α
Non-Repetitive Peak Forward Surge Current 8.38 Half Sine-Wave Superimposed on Rated Load	ms Single	I _{FSM}	150	Α
Operating Junction Temperature		TJ	-65 ~ +150	°C
Storage Temperature		T_{STG}	-65 ~ + 150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS (PER LEG)

PARAMETER	SYMBOL	RATINGS	UNIT
Typical Thermal Resistance	$\theta_{ m JC}$	4	°C/W

■ ELECTRICAL CHARACTERISTICS (PER LEG) (T_A=25°C unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage	$V_{(BR)R}$	I _R =0.50mA	150			٧
Forward Voltage Drop	V_{FM}	I _F =5A, T _J =25°C			0.84	V
		I _F =5A, T _J =125°C			0.68	V
Leakage Current	DM	V _R =150V, T _J =25°C			100	μΑ
		V _R =150V, T _J =125°C			10	mA

Note: Pulse Test: Pulse width $\leq 300 \mu s$, Duty cycle $\leq 2\%$.

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